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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10042732	FILING DATE 04/25/2001	CLASS 257 438	SUBCLASS 618	GAU 2815	EXAMINER Thanh Nguyen
**APPLICANTS: Craig Sander; Rich Klein; Asim Selcuk; Nicholas Kepler; Christopher Spence; Raymond Lee; John Holst; Stephen Horne; 2813					
**CONTINUING DATA VERIFIED: This application is a DIV of 09/515,875 02/29/2000 PAT 6,287,953 which is a CIP of 09/119,934 07/21/1998 PAT 6,146,954 TN Ngye (2813)					
** FOREIGN APPLICATIONS VERIFIED: TN					
PG-PUB		DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		039153-0433 (C167596-CIP)	
Verified and Acknowledged Examiners's initials		TN			
TITLE : Minimizing transistor size in integrated circuits					
U.S. DEPT. OF COMM./PAT. & TM-PTO-435L (Rev. 12-94)					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Fig. Drawg. Print Fig.
<input type="checkbox"/> TERMINAL		Application Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
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